



Appl. No. 10/609,311

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application Serial No. ....10/609,311  
Filing Date ..... June 26, 2003  
Inventor..... Donald L. Yates  
Assignee ..... Micron Technology, Inc.  
Group Art Unit..... 1756  
Examiner ..... Nicole M. Barreca  
Attorney's Docket No. ....MI22-2236  
Customer No. ....021567  
Title:..... Semiconductor Processing Patterning Methods

**RESPONSE TO JANUARY 23, 2006 FINAL OFFICE ACTION  
TO ACCOMPANY REQUEST FOR CONTINUED EXAMINATION**

To: Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

From: Robert C. Hyta  
Wells St. John P.S.  
601 W. First Avenue, Suite 1300  
Spokane, WA 99201-3828  
Tel. (509) 624-4276; Fax (509) 838-3424

**AMENDMENTS**